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; Date : October 2003

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; File : DACquadr.asm

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; Hardware : ADuC842

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; Description : Outputs sine waves on DAC0 and DAC1 at 590Hz.

; Output signals are in quadrature with eachother,

; DAC1 leading DAC0 by 90 degrees. since each DAC is

; updated when its DACxL register is written to, they

; are not updated at the exact same moment, and a

; phase error of (in this case) 0.625degrees results.

; to address this problem, see code: "DACsync.asm".

; Rate calculations assume a core frequency of 2.09MHz

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$MOD842 ; Use 8052&ADuC842 predefined symbols

LED EQU P3.4 ; P3.4 drives red LED on eval board

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; BEGINNING OF CODE

CSEG

ORG 0000h

MOV ADCCON1,#80H

MOV PLLCON,#03h ; PLL to default freq

MOV DACCON,#01Fh ; both DACs on,12bit,asynchronous

MOV DAC0H,#008h

MOV DAC0L,#000h ; DAC0 to mid-scale

MOV DAC1H,#00Fh

MOV DAC1L,#0FFh ; DAC1 to full-scale

MOV DPTR,#TABLE

STEP: CLR A ;

MOVC A,@A+DPTR ; get high byte for mainDAC..

MOV DAC0H,A ; ..and move it into DAC0 register

MOV A,#020h ; offset by 90deg for quadratureDAC

MOVC A,@A+DPTR ; get high byte for quadratureDAC..

MOV DAC1H,A ; ..and move it into DAC1 register

INC DPTR ; move on to get low bytes

CLR A ;

MOVC A,@A+DPTR ; get low byte for mainDAC..

MOV DAC0L,A ; ..and update DAC0

MOV A,#020h ; offset by 90deg for quadratureDAC

MOVC A,@A+DPTR ; get low byte for quadratureDAC..

MOV DAC1L,A ; ..and update DAC1

INC DPTR ; move on for next data point

ANL DPL,#07Fh ; wrap around at end of table

MOV A,DAC0H ;

MOV C,ACC.3 ; MSB of DAC0 value

MOV LED,C ; LED = MSB of DAC0

NOP ;

NOP ;

NOP ;

NOP ;

NOP ;

NOP ;

NOP ;

NOP ;

JMP STEP ;

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; SINE LOOKUP TABLE

ORG 01000h

TABLE:

DB 007h, 0FFh

DB 008h, 0C8h

DB 009h, 08Eh

DB 00Ah, 051h

DB 00Bh, 00Fh

DB 00Bh, 0C4h

DB 00Ch, 071h

DB 00Dh, 012h

DB 00Dh, 0A7h

DB 00Eh, 02Eh

DB 00Eh, 0A5h

DB 00Fh, 00Dh

DB 00Fh, 063h

DB 00Fh, 0A6h

DB 00Fh, 0D7h

DB 00Fh, 0F5h

DB 00Fh, 0FFh

DB 00Fh, 0F5h

DB 00Fh, 0D7h

DB 00Fh, 0A6h

DB 00Fh, 063h

DB 00Fh, 00Dh

DB 00Eh, 0A5h

DB 00Eh, 02Eh

DB 00Dh, 0A7h

DB 00Dh, 012h

DB 00Ch, 071h

DB 00Bh, 0C4h

DB 00Bh, 00Fh

DB 00Ah, 051h

DB 009h, 08Eh

DB 008h, 0C8h

DB 007h, 0FFh

DB 007h, 036h

DB 006h, 070h

DB 005h, 0ADh

DB 004h, 0EFh

DB 004h, 03Ah

DB 003h, 08Dh

DB 002h, 0ECh

DB 002h, 057h

DB 001h, 0D0h

DB 001h, 059h

DB 000h, 0F1h

DB 000h, 09Bh

DB 000h, 058h

DB 000h, 027h

DB 000h, 009h

DB 000h, 000h

DB 000h, 009h

DB 000h, 027h

DB 000h, 058h

DB 000h, 09Bh

DB 000h, 0F1h

DB 001h, 059h

DB 001h, 0D0h

DB 002h, 057h

DB 002h, 0ECh

DB 003h, 08Dh

DB 004h, 03Ah

DB 004h, 0EFh

DB 005h, 0ADh

DB 006h, 070h

DB 007h, 036h ; end of table

DB 007h, 0FFh ; repeat first 90degrees for quadratureDAC

DB 008h, 0C8h

DB 009h, 08Eh

DB 00Ah, 051h

DB 00Bh, 00Fh

DB 00Bh, 0C4h

DB 00Ch, 071h

DB 00Dh, 012h

DB 00Dh, 0A7h

DB 00Eh, 02Eh

DB 00Eh, 0A5h

DB 00Fh, 00Dh

DB 00Fh, 063h

DB 00Fh, 0A6h

DB 00Fh, 0D7h

DB 00Fh, 0F5h

DB 00Fh, 0FFh

;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

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